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October 24, 2005

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Re: **CERTIFICATE OF CORRECTION**  
U. S. Letters Patent No. 6,914,335  
Issued: July 5, 2005  
For: **SEMICONDUCTOR DEVICE HAVING A  
LOW-K DIELECTRIC LAYER**  
Inventor: Andideh, et al.  
Our File No. 42390.P8125D

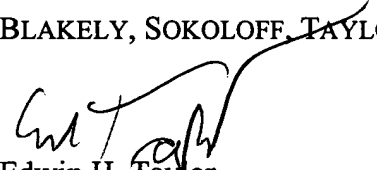
**Certificate**  
**NOV 10 2005**  
**of Correction**

Dear Sir:

Enclosed is the Certificate of Correction (two copies) for the above-referenced patent.  
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.  
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

  
Edwin H. Taylor  
Reg. No. 25,129

EHT/cgb  
Enclosures

NOV 14 2005

**UNITED STATES PATENT AND TRADEMARK OFFICE**  
**CERTIFICATE OF CORRECTION**

**PATENT NO.** : 6,914,335  
**DATED** : July 5, 2005  
**INVENTOR(S)** : Andideh, et. al

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 6, at line 13, delete "arid" and insert --and--.

In column 6, at line 26, delete "SiOP," and insert -SiOF,--.

**MAILING ADDRESS OF SENDER**  
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Los Angeles, CA 90025-1026

**PATENT NO.** 6,914,335

**Certificate of Correction (PTO Form 1050)-Amended**

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